

FIG. 1 (PRIOR ART)

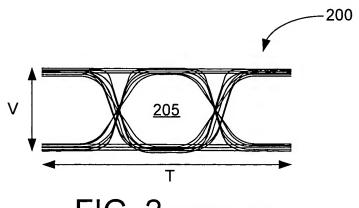
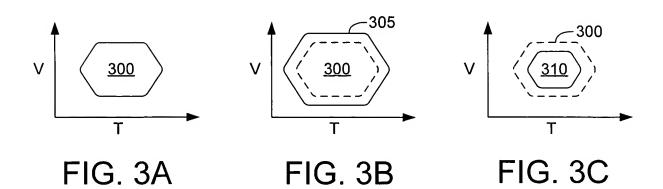
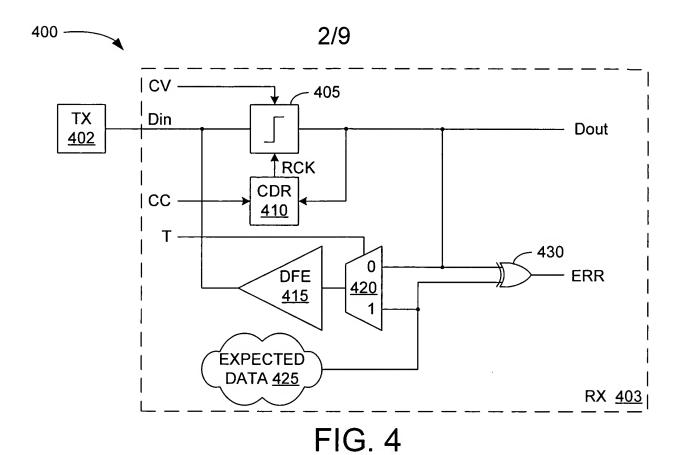
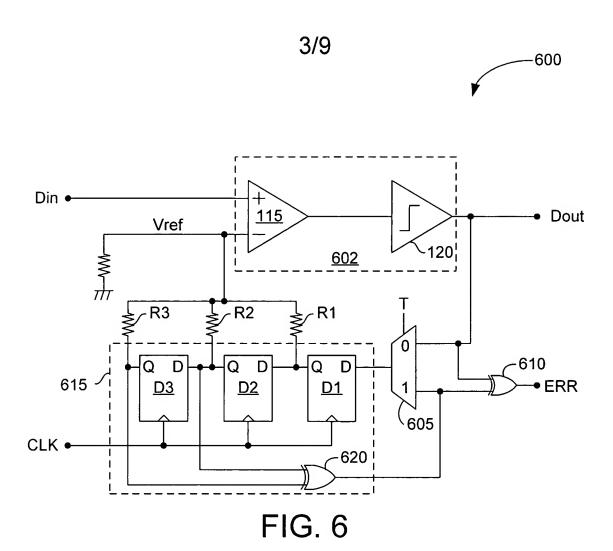


FIG. 2 (PRIOR ART)





500 -515 505 -<u>510</u> CV1 -0 430 TRCK1 - ERR CDR <u>520</u> ↓RCK2 CV2 -Dout Din -405 -DFE <u>415</u> FIG. 5



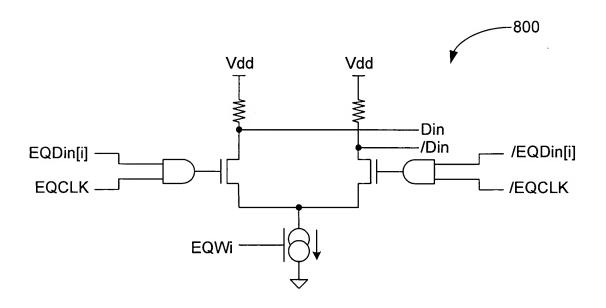
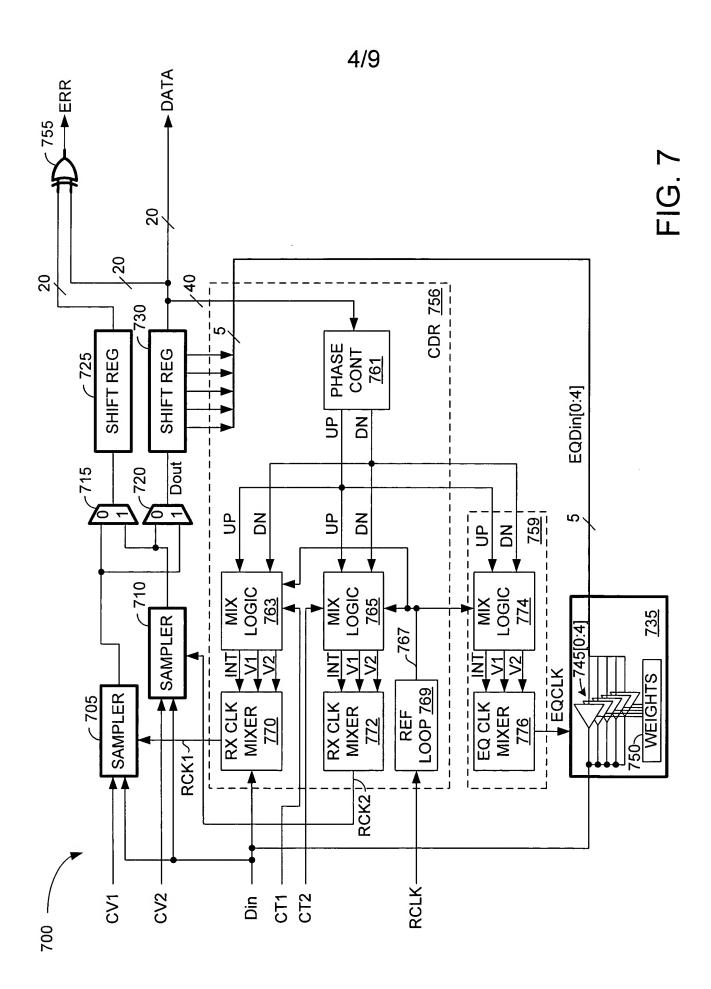
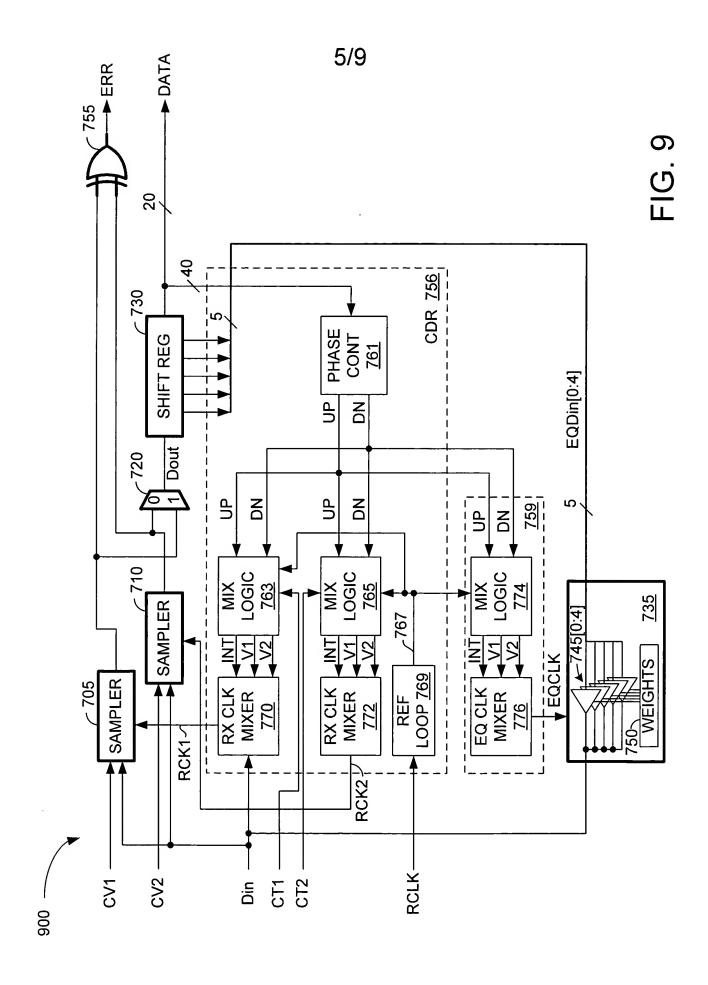


FIG. 8





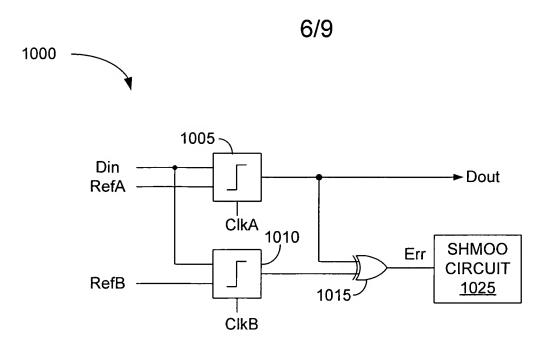


FIG. 10A

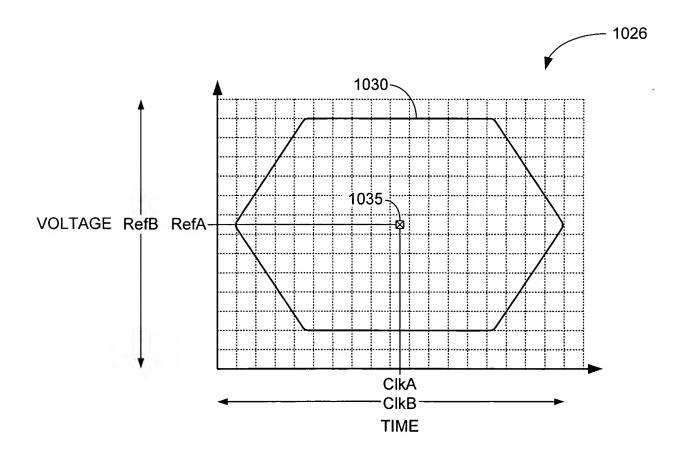
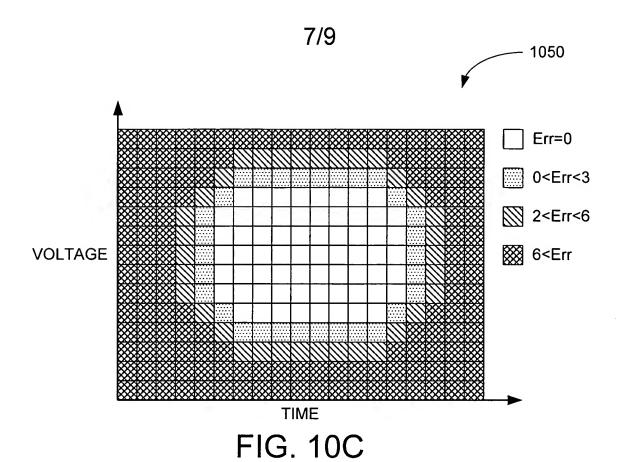


FIG. 10B



Err D Q D Q Out ClkA R R R

FIG. 11

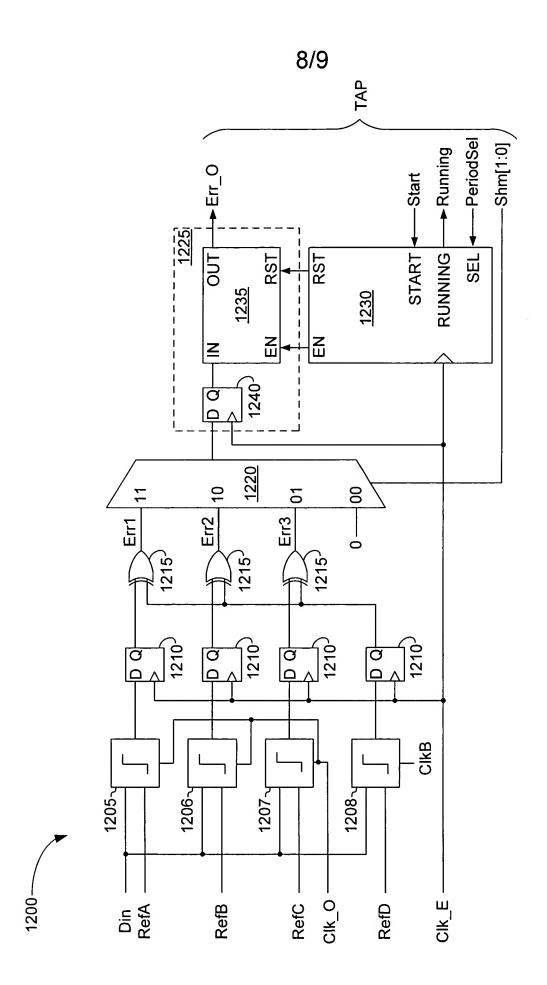


FIG. 12

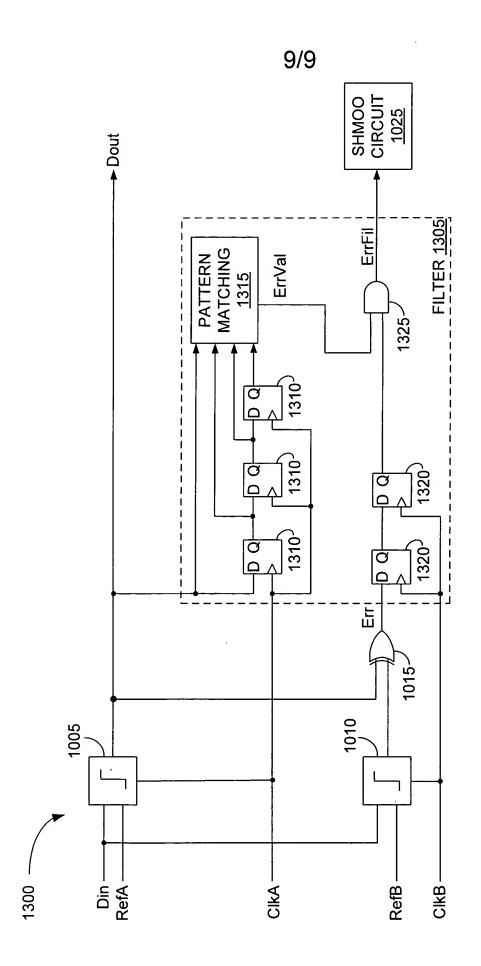


FIG. 13